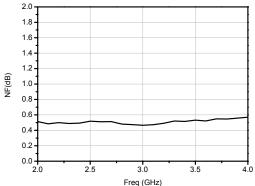


GaAs MMIC LOW NOISE AMPLIFIER,2 - 4GHz

Noise Figure

2.0 1.8 16 1.4 1.2 1.0 0.8 0.6 0.4 0.2



Features

Freq: 2~4GHz Gain: 29.5dB

Noise Figure: 0.5dB

Output Power for 1 dB Compression:12.5dBm

Supply Voltage: +5V Supply Current: 28mA

Chip Size:1.85mm×1.2mm×0.1mm

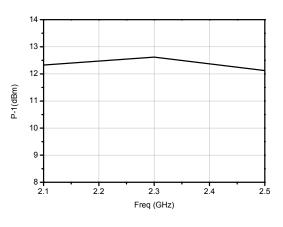
General Description

The HG113FC-2 is a GaAs pHEMT MMIC Low Noise Amplifier operating between 2 and 4 GHz. The LNA has been optimized to provide 29.5dB gain, 0.5dB noise figure and 12.5 dBm output power for 1dB compression.

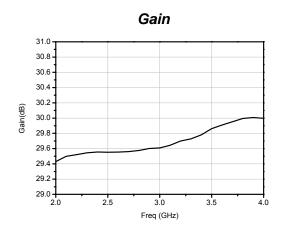
Electrical Specifications(T_A =25 C, Vdd= +5V).

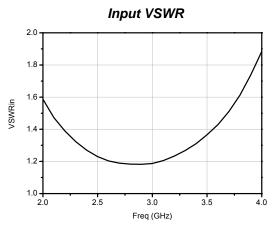
Parameter	Min.	Тур.	Max.
Freq(GHz)	2~4		
Gain (dB)		29.5	1
Gain Flatness (dB)	_	±0.8	_
Input VSWR	_	1.4	_
Output VSWR	_	1.3	_
Noise Figure(dB)	_	0.5	_
Output Power for 1 dB		12.5	
Compression(dBm)	_	12.0	_

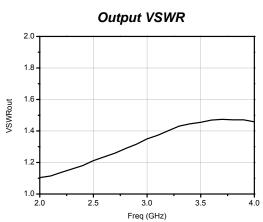
Output Power for 1dB Compression



Measured Performance



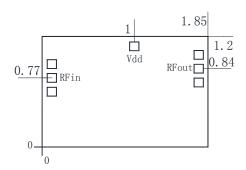




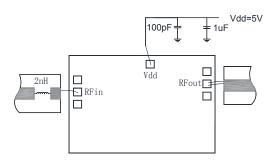


GaAs MMIC LOW NOISE AMPLIFIER,2 - 4GHz

Outline Drawing (mm)



Assembly Diagram



Absolute Maximum Ratings

Supply Voltage	+5.5V
RF Input Power	+18dBm
Operating Temperature	-55℃~125℃
Storage Temperature	-65℃~150℃

Notes:

- 1. The chip should be stored in a dry and nitrogen environment, and used in a clean environment.
- 2. GaAs material is brittle, can not touch the surface of the chip, must be careful when using.
- 3. The chip is welding with conductive adhesive or alloy (alloy temperature should not exceed 300 $^{\circ}$ C, and no more than 30 sec.), and should make it fully grounded.
- 4.The chip microwave port and substrate gap is not exceeding 0.05mm, with $\Phi25\mu m$ double gold wire bonding, suggested length of gold wire 250 $\sim\!400\mu m$.
- 5. Chip microwave port with a DC blocking capacitor.
- 6. The chip is sensitive to static electricity, and should be protected against static electricity during storage and use.