



Features

Freq: 1.4~3GHz Gain: 36dB

Noise Figure: 0.8dB

Output Power for 1 dB Compression:18dBm

Supply Voltage: +5V Supply Current:100mA

Chip Size:1.57mm×1.25mm×0.1mm

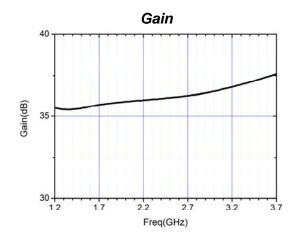
General Description

The HG113FB-3 is a GaAs pHEMT MMIC Low Noise Amplifier operating between 1.4 and 3GHz. The LNA has been optimized to provide 36dB gain, 0.8dB noise figure and 18 dBm output power for 1dB compression. The external DC blocking capacitors and choke inductor are required. See assembly diagram.

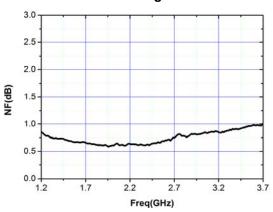
Electrical Specifications(T_A =25 C, Vdd= +5V).

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Parameter	Min.	Тур.	Max.
Freq(GHz)		1.4~3	
Gain (dB)	_	36	_
Gain Flatness (dB)	_	±0.5	_
Input VSWR	_	1.5	_
Output VSWR	_	1.2	_
Noise Figure(dB)	_	0.8	_
Output Power for 1 dB	_	18	_
Compression(dBm)			

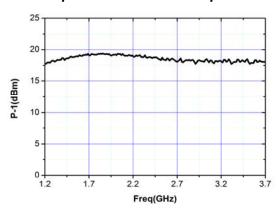
Measured Performance

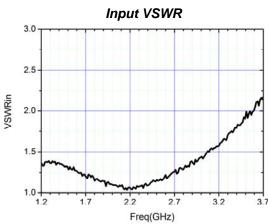


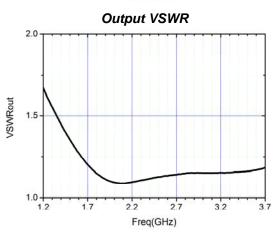
Noise Figure



Output Power for 1dB Compression

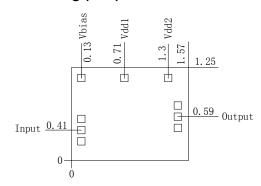




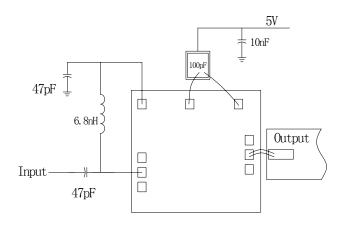




Outline Drawing (mm)



Assembly Diagram



Absolute Maximum Ratings

Supply Voltage	+5.5V	
RF Input Power	+18dBm	
Operating Temperature	-55°C∼125°C	
Storage Temperature	-65℃~150℃	

Notes:

- 1. The chip should be stored in a dry and nitrogen environment, and used in a clean environment.
- 2. GaAs material is brittle, can not touch the surface of the chip, must be careful when using.
- 3. The chip is welding with conductive adhesive or alloy (alloy temperature should not exceed 300° C, and no more than 30 sec.), and should make it fully grounded.
- 4.The chip microwave port and substrate gap is not exceeding 0.05mm, with $\Phi25\mu m$ double gold wire bonding, suggested length of gold wire 250 $\sim\!400\mu m.$
- 5. Chip microwave port without DC blocking capacitor.
- 6. The chip is sensitive to static electricity, and should be protected against static electricity during storage and use.