

GaAs MMIC LOW NOISE AMPLIFIER,1 - 3GHz

Features

Freq: 1~3GHz Gain: 25dB

Noise Figure: 1.2dB

Output Power for 1 dB Compression:14dBm

Supply Voltage: +5V Supply Current: 55mA

Chip Size:1.4mm×1.25mm×0.1mm

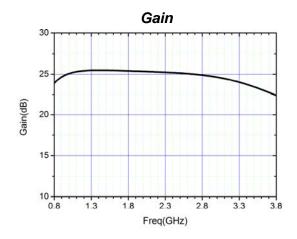
General Description

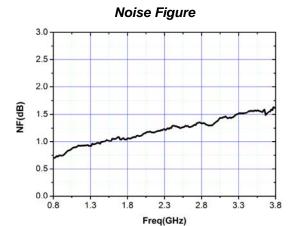
The HG113FH is a GaAs pHEMT MMIC Low Noise Amplifier operating between 1 and 3GHz. The LNA has been optimized to provide 25dB gain, 1.2dB noise figure and 14dBm output power for 1dB compression.

Electrical Specifications(T_A =25 C, Vdd= +5V).

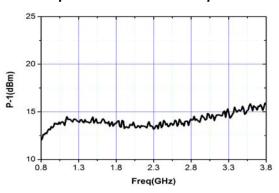
Parameter	Min.	Тур.	Max.
Freq(GHz)	1~3		
Gain (dB)	_	25	_
Gain Flatness (dB)	_	±0.5	_
Input VSWR	_	1.5	_
Output VSWR	_	1.3	_
Noise Figure(dB)	_	1.2	_
Output Power for 1 dB		14	
Compression(dBm)	_	14	_

Measured Performance

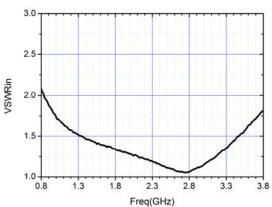




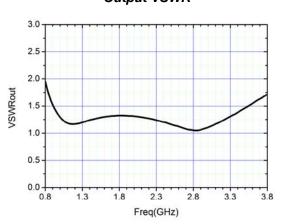
Output Power for 1dB Compression



Input VSWR



Output VSWR



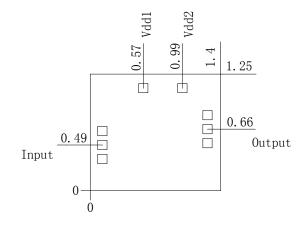


GaAs MMIC LOW NOISE AMPLIFIER,1 - 3GHz

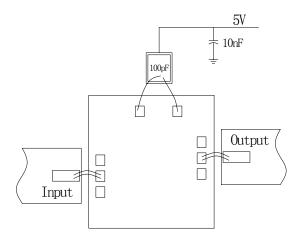
Absolute Maximum Ratings

Supply Voltage	+5.5V
RF Input Power	+18dBm
Operating Temperature	-55℃~125℃
Storage Temperature	-65℃~150℃

Outline Drawing (mm)



Assembly Diagram



Notes:

- 1. The chip should be stored in a dry and nitrogen environment, and used in a clean environment.
- 2. GaAs material is brittle, can not touch the surface of the chip, must be careful when using.
- 3. The chip is welding with conductive adhesive or alloy (alloy temperature should not exceed 300° C, and no more than 30 sec.), and should make it fully grounded.
- 4.The chip microwave port and substrate gap is not exceeding 0.05mm, with Φ 25 μ m double gold wire bonding, suggested length of gold wire 250 \sim 400 μ m.
- 5. Chip microwave port with a DC blocking capacitor.
- 6. The chip is sensitive to static electricity, and should be protected against static electricity during storage and use.